

FIG. 3

File Information

Topology

Sequence

Parameters

GPIO

Advanced

Evaluation Board

Margining

Monitoring

Power converters to add to Input

Find converters by

Voltage

Current

Make

34

35

31

36

New custom

Add to rail

|   | Model | Voltage | Current | Type  | Package |
|---|-------|---------|---------|-------|---------|
| 0 | XXX1  | 1.8     | 40      | Brick | n/a     |
| 1 | XXX2  | 5.0     | 25      | Brick | n/a     |
| 2 | XXX3  | 3.3     | 35      | Brick | n/a     |
| 3 | YY1   | 1.5     | 10      | VRM   | SMT     |
| 4 | YY2   | 2.5     | 10      | VRM   | SMT     |
| 5 | Z1    | 2.0     | 0.1     | LDO   | SOIC    |

33

Input

48V

30

Details of Converter

Make

Model

Location

Move

Delete

Details of Rail

Label

Nominal Output

Volts

Amps

Margining

Minimum

Maximum

Volts

Volts

☐ Trim on primary side

Monitoring

32

FIG. 4

File Information

Topology

Sequence

Parameters

GPIO

Advanced

Evaluation Board

Margining

Monitoring

Power converters to add to Rail V0

Find converters by

Voltage

Current

Make

34

35

31

36

New custom

Add to rail

|   | Model | Voltage | Current | Type  | Package |
|---|-------|---------|---------|-------|---------|
| 0 | XXX1  | 1.8     | 40      | Brick | n/a     |
| 1 | XXX2  | 5.0     | 25      | Brick | n/a     |
| 2 | XXX3  | 3.3     | 35      | Brick | n/a     |
| 3 | YY1   | 1.5     | 10      | VRM   | SMT     |
| 4 | YY2   | 2.5     | 10      | VRM   | SMT     |
| 5 | Z1    | 2.0     | 0.1     | LDO   | SOIC    |

33

Input

48V

PC0

36

Brick

Rail V0

1.8V 40A

30

Details of Converter PC0

Make

Model

Location

Move

Maker1

XXX1

Main Board

Delete

Details of Rail V0

Label

Nominal Output

V0

1.8

40

Volts

Amps

Margining

Minimum

Maximum

Trim on primary side

1.75

1.85

Volts

Volts

Monitoring

32

FIG. 5

File Information

Topology

Sequence

Parameters

GPIO

Advanced

Evaluation Board

Margining

Monitoring

Power converters to add to Rail V1

Find converters by

Voltage

Current

Make

34

35

31

36

New custom

Add to rail

| Model  | Voltage | Current | Type  | Package |
|--------|---------|---------|-------|---------|
| 0 XXX1 | 1.8     | 40      | Brick | n/a     |
| 1 XXX2 | 5.0     | 25      | Brick | n/a     |
| 2 XXX3 | 3.3     | 35      | Brick | n/a     |
| 3 YY1  | 1.5     | 10      | VRM   | SMT     |
| 4 YY2  | 2.5     | 10      | VRM   | SMT     |
| 5 Z1   | 2.0     | 0.1     | LDO   | SOIC    |

33

Input

48V

PC0

Rail V0

Brick

1.8V 40A

PC1

Rail V1

Brick

5.0V 25A

PC2

VRM

Rail V2

1.5V 10A

30

Details of Converter PC1

Make

Model

Location

Move

Maker2

XXX2

Main Board

Delete

Details of Rail V1

Label

Nominal Output

V1

5.0

25

Volts

Amps

Margining

Minimum

Maximum

4.95

5.05

Volts

Volts

☐ Trim on primary side

Monitoring

32

FIG. 6

File Information

Topology

Sequence

Parameters

GPIO

Advanced

Evaluation Board

Margining

Monitoring

Power converters to add to Rail V5

Find converters by

Voltage

Current

Make

| Model  | Voltage | Current | Type  | Package |
|--------|---------|---------|-------|---------|
| 0 XXX1 | 1.8     | 40      | Brick | n/a     |
| 1 XXX2 | 5.0     | 25      | Brick | n/a     |
| 2 XXX3 | 3.3     | 35      | Brick | n/a     |
| 3 YY1  | 1.5     | 10      | VRM   | SMT     |
| 4 YY2  | 2.5     | 10      | VRM   | SMT     |
| 5 Z1   | 2.0     | 0.1     | LDO   | SOIC    |

35

31

36

New custom

Add to rail

33

Input

48V

PC0

Rail V0

1.8V 40A

Brick

PC1

Rail V1

5.0V 25A

Brick

PC2

Rail V2

1.5V 10A

VRM

PC3

Rail V3

2.5V 10A

VRM

PC4

Rail V4

2.0V 0.1A

LDO

PC5

Rail V5

3.3V 35A

Brick

30

Details of Converter PC5

Make

Model

Location

Move

Maker3

XXX3

Main Board

Delete

Details of Rail V5

Label

Nominal Output

Volts

Amps

V5

3.3

35

Margining

Minimum

Maximum

Trim on primary side

3.2

3.4

Monitoring

32

FIG. 7

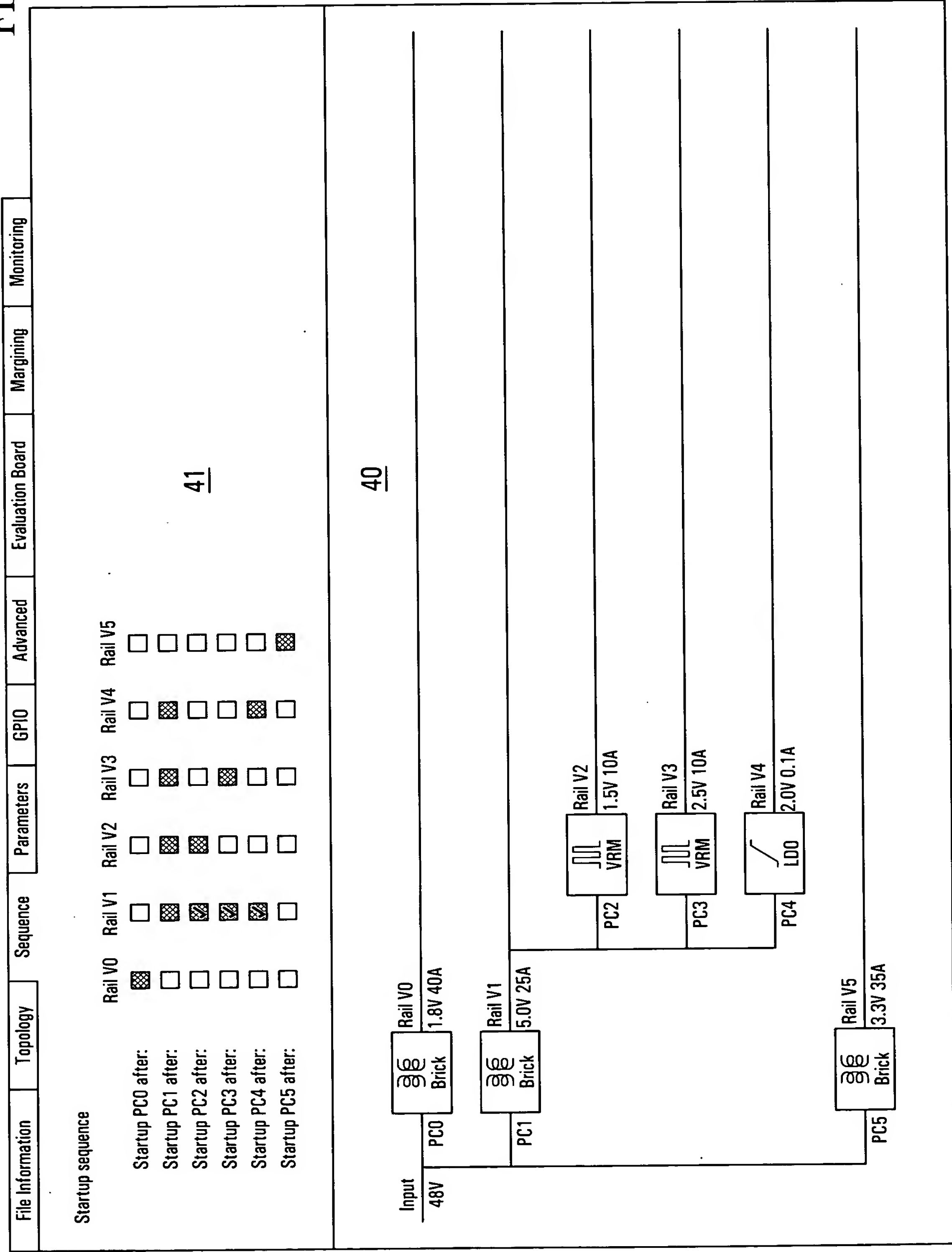


FIG. 8

| File Information | Topology | Sequence | Parameters | GPIO | Advanced | Evaluation Board | Margining | Monitoring |
|------------------|----------|----------|------------|------|----------|------------------|-----------|------------|
|------------------|----------|----------|------------|------|----------|------------------|-----------|------------|

Startup sequence

|                    | Rail V0                             | Rail V1                             | Rail V2                             | Rail V3                             | Rail V4                             | Rail V5                             |
|--------------------|-------------------------------------|-------------------------------------|-------------------------------------|-------------------------------------|-------------------------------------|-------------------------------------|
| Startup PC0 after: | <input checked="" type="checkbox"/> | <input type="checkbox"/>            | <input type="checkbox"/>            | <input type="checkbox"/>            | <input type="checkbox"/>            | <input type="checkbox"/>            |
| Startup PC1 after: | <input type="checkbox"/>            | <input checked="" type="checkbox"/> | <input checked="" type="checkbox"/> | <input checked="" type="checkbox"/> | <input checked="" type="checkbox"/> | <input checked="" type="checkbox"/> |
| Startup PC2 after: | <input type="checkbox"/>            | <input checked="" type="checkbox"/> | <input checked="" type="checkbox"/> | <input type="checkbox"/>            | <input type="checkbox"/>            | <input type="checkbox"/>            |
| Startup PC3 after: | <input type="checkbox"/>            | <input checked="" type="checkbox"/> | <input type="checkbox"/>            | <input checked="" type="checkbox"/> | <input type="checkbox"/>            | <input type="checkbox"/>            |
| Startup PC4 after: | <input type="checkbox"/>            | <input checked="" type="checkbox"/> | <input type="checkbox"/>            | <input type="checkbox"/>            | <input checked="" type="checkbox"/> | <input checked="" type="checkbox"/> |
| Startup PC5 after: | <input type="checkbox"/>            | <input type="checkbox"/>            | <input type="checkbox"/>            | <input type="checkbox"/>            | <input checked="" type="checkbox"/> | <input checked="" type="checkbox"/> |

41

40

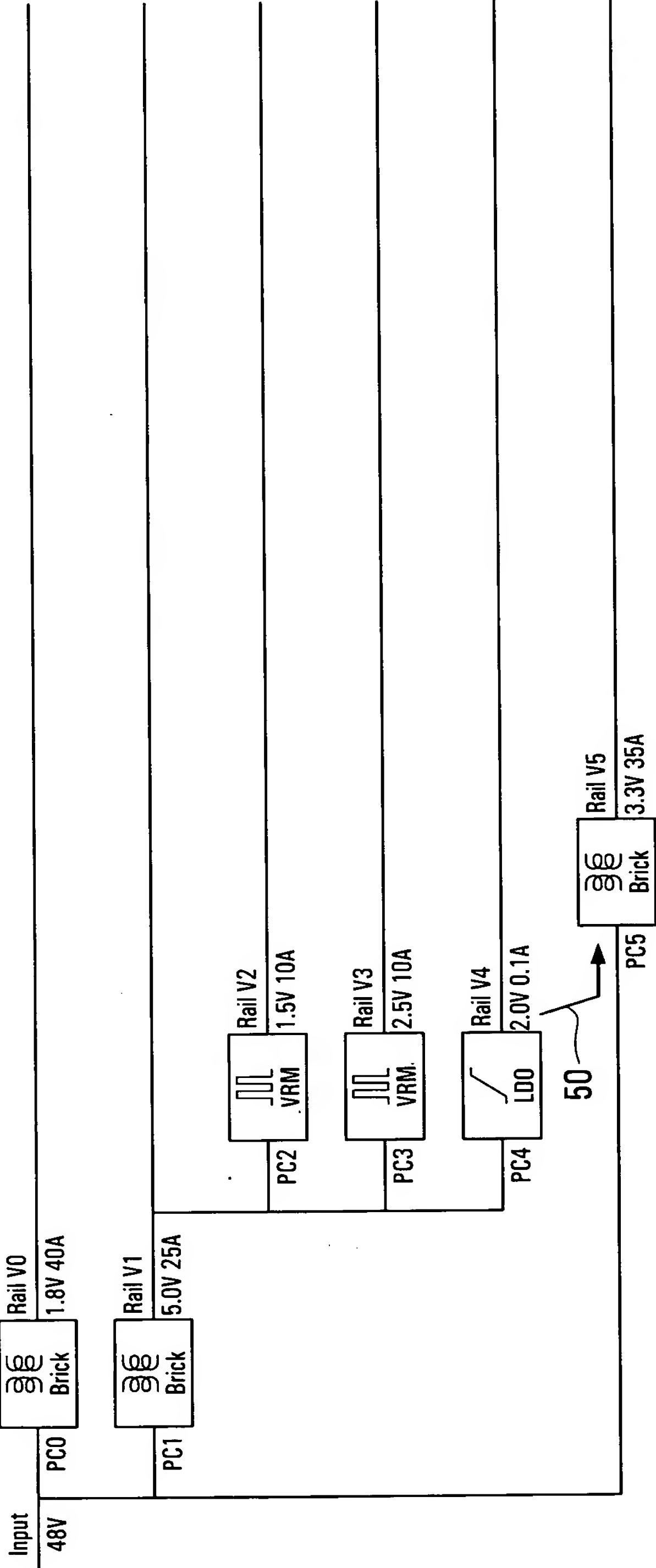


FIG. 9

| File Information | Topology | Sequence | Parameters | GPIO | Advanced | Evaluation Board | Margining | Monitoring |
|------------------|----------|----------|------------|------|----------|------------------|-----------|------------|
|------------------|----------|----------|------------|------|----------|------------------|-----------|------------|

Startup sequence

|                    | Rail V0                             | Rail V1                             | Rail V2                             | Rail V3                             | Rail V4                             | Rail V5                             |
|--------------------|-------------------------------------|-------------------------------------|-------------------------------------|-------------------------------------|-------------------------------------|-------------------------------------|
| Startup PC0 after: | <input checked="" type="checkbox"/> | <input checked="" type="checkbox"/> | <input checked="" type="checkbox"/> | <input checked="" type="checkbox"/> | <input checked="" type="checkbox"/> | <input checked="" type="checkbox"/> |
| Startup PC1 after: | <input checked="" type="checkbox"/> | <input checked="" type="checkbox"/> | <input checked="" type="checkbox"/> | <input checked="" type="checkbox"/> | <input checked="" type="checkbox"/> | <input checked="" type="checkbox"/> |
| Startup PC2 after: | <input type="checkbox"/>            | <input checked="" type="checkbox"/> | <input type="checkbox"/>            | <input type="checkbox"/>            | <input type="checkbox"/>            | <input type="checkbox"/>            |
| Startup PC3 after: | <input type="checkbox"/>            | <input checked="" type="checkbox"/> | <input checked="" type="checkbox"/> | <input type="checkbox"/>            | <input type="checkbox"/>            | <input checked="" type="checkbox"/> |
| Startup PC4 after: | <input type="checkbox"/>            | <input checked="" type="checkbox"/> | <input type="checkbox"/>            | <input type="checkbox"/>            | <input checked="" type="checkbox"/> | <input checked="" type="checkbox"/> |
| Startup PC5 after: | <input type="checkbox"/>            | <input type="checkbox"/>            | <input type="checkbox"/>            | <input type="checkbox"/>            | <input checked="" type="checkbox"/> | <input checked="" type="checkbox"/> |

41

40

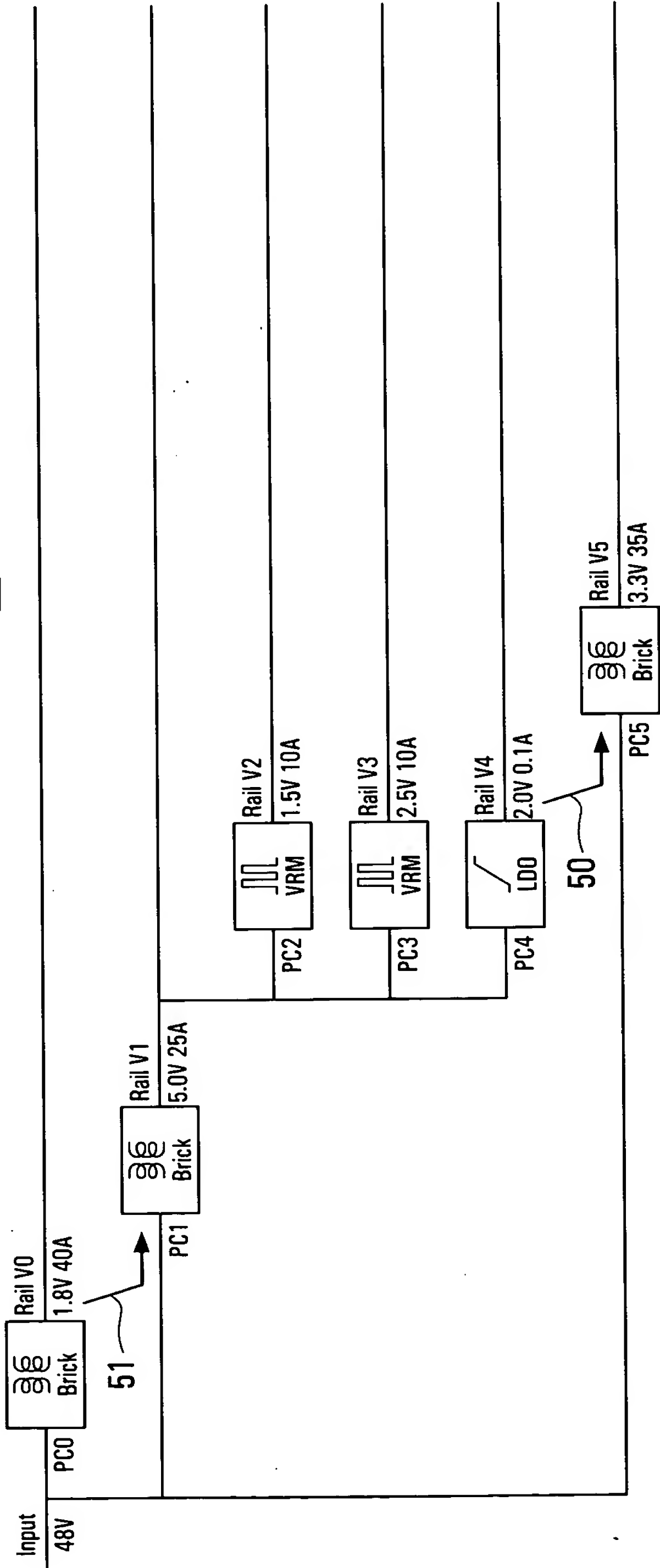




FIG. 10

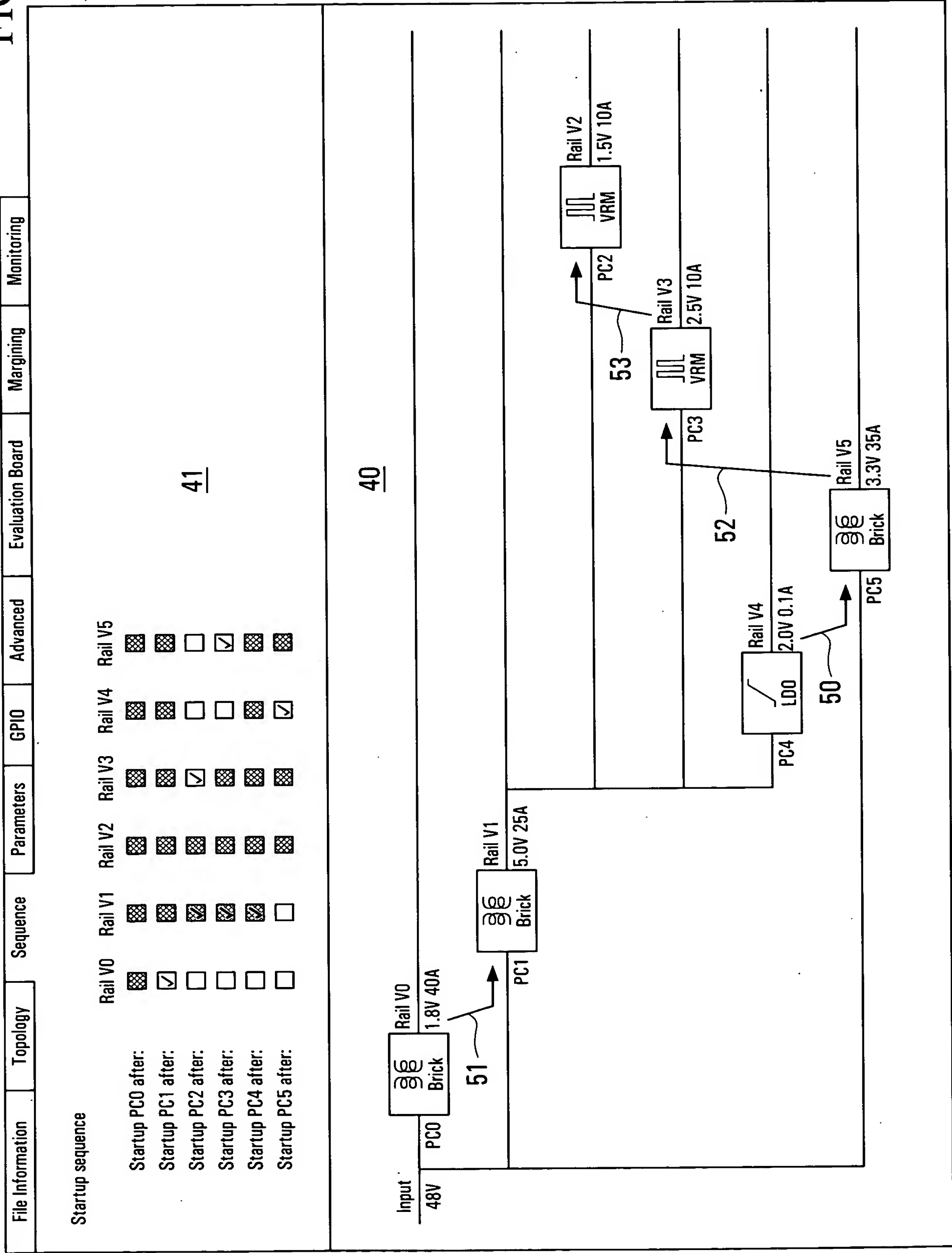


FIG. 11

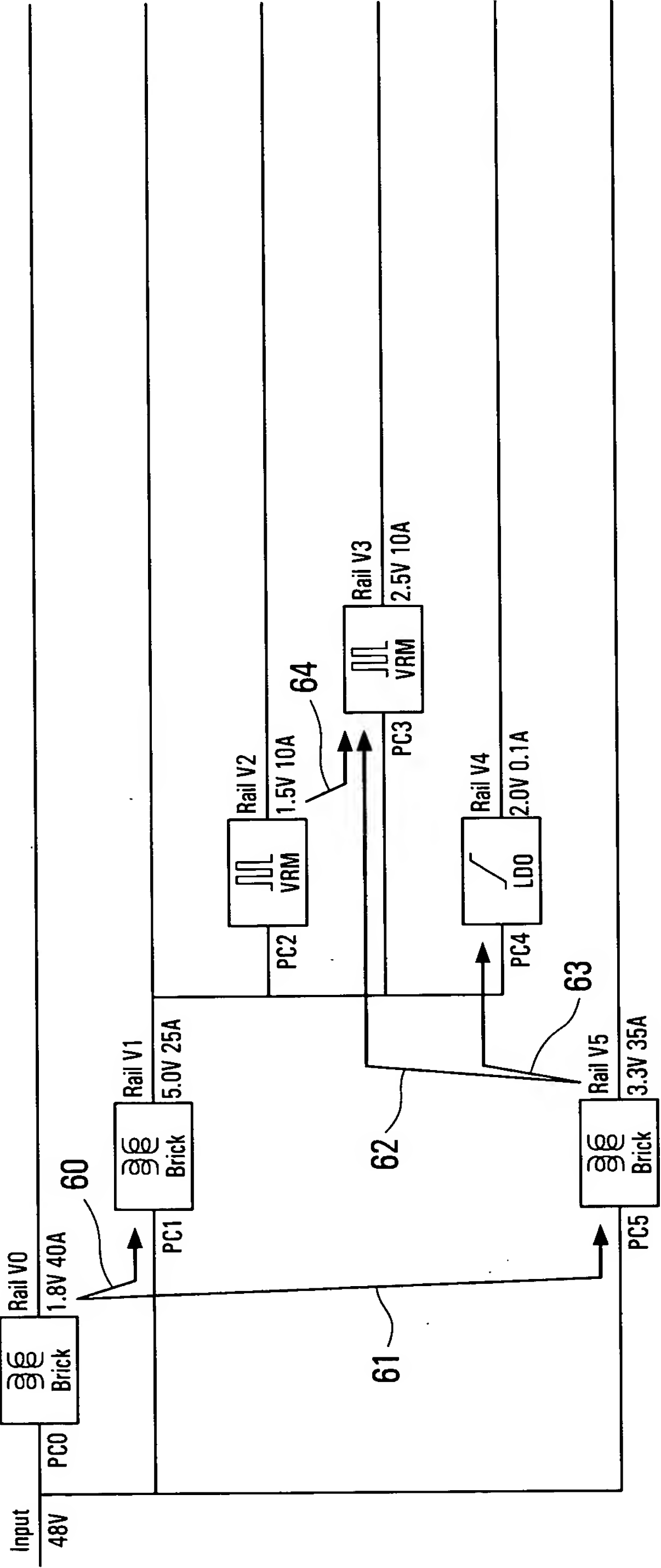
| File Information | Topology | Sequence | Parameters | GPIO | Advanced | Evaluation Board | Margining | Monitoring |
|------------------|----------|----------|------------|------|----------|------------------|-----------|------------|
|------------------|----------|----------|------------|------|----------|------------------|-----------|------------|

Startup sequence

|                    | Rail V0                             | Rail V1                             | Rail V2                             | Rail V3                             | Rail V4                             | Rail V5                             |
|--------------------|-------------------------------------|-------------------------------------|-------------------------------------|-------------------------------------|-------------------------------------|-------------------------------------|
| Startup PC0 after: | <input checked="" type="checkbox"/> | <input checked="" type="checkbox"/> | <input checked="" type="checkbox"/> | <input checked="" type="checkbox"/> | <input checked="" type="checkbox"/> | <input checked="" type="checkbox"/> |
| Startup PC1 after: | <input checked="" type="checkbox"/> | <input checked="" type="checkbox"/> | <input checked="" type="checkbox"/> | <input checked="" type="checkbox"/> | <input checked="" type="checkbox"/> | <input type="checkbox"/>            |
| Startup PC2 after: | <input type="checkbox"/>            | <input checked="" type="checkbox"/> | <input checked="" type="checkbox"/> | <input checked="" type="checkbox"/> | <input type="checkbox"/>            | <input type="checkbox"/>            |
| Startup PC3 after: | <input type="checkbox"/>            | <input checked="" type="checkbox"/> | <input checked="" type="checkbox"/> | <input checked="" type="checkbox"/> | <input type="checkbox"/>            | <input checked="" type="checkbox"/> |
| Startup PC4 after: | <input type="checkbox"/>            | <input checked="" type="checkbox"/> | <input type="checkbox"/>            | <input type="checkbox"/>            | <input checked="" type="checkbox"/> | <input checked="" type="checkbox"/> |
| Startup PC5 after: | <input checked="" type="checkbox"/> | <input type="checkbox"/>            | <input type="checkbox"/>            | <input checked="" type="checkbox"/> | <input checked="" type="checkbox"/> | <input checked="" type="checkbox"/> |

41

40



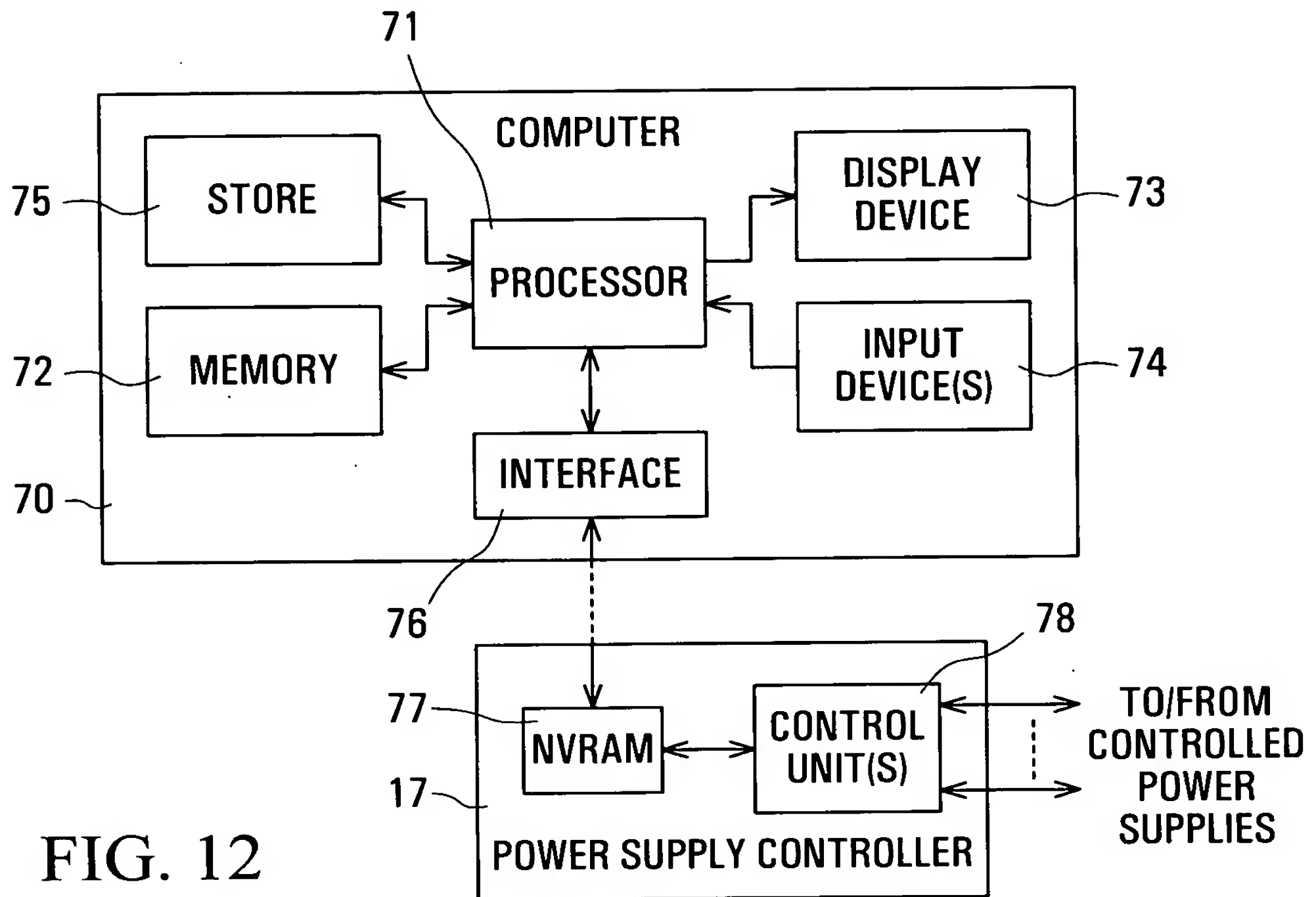


FIG. 13

| BIT:      | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
|-----------|---|---|---|---|---|---|---|---|
| CONFIG    | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 |
| STRTEN(0) | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 |
| SHDNEN(0) | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 |
| STRTEN(1) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 |
| SHDNEN(1) | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 |
| STRTEN(2) | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 |
| SHDNEN(2) | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 |
| STRTEN(3) | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 |
| SHDNEN(3) | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 |
| STRTEN(4) | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 |
| SHDNEN(4) | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 |
| STRTEN(5) | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 |
| SHDNEN(5) | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 |